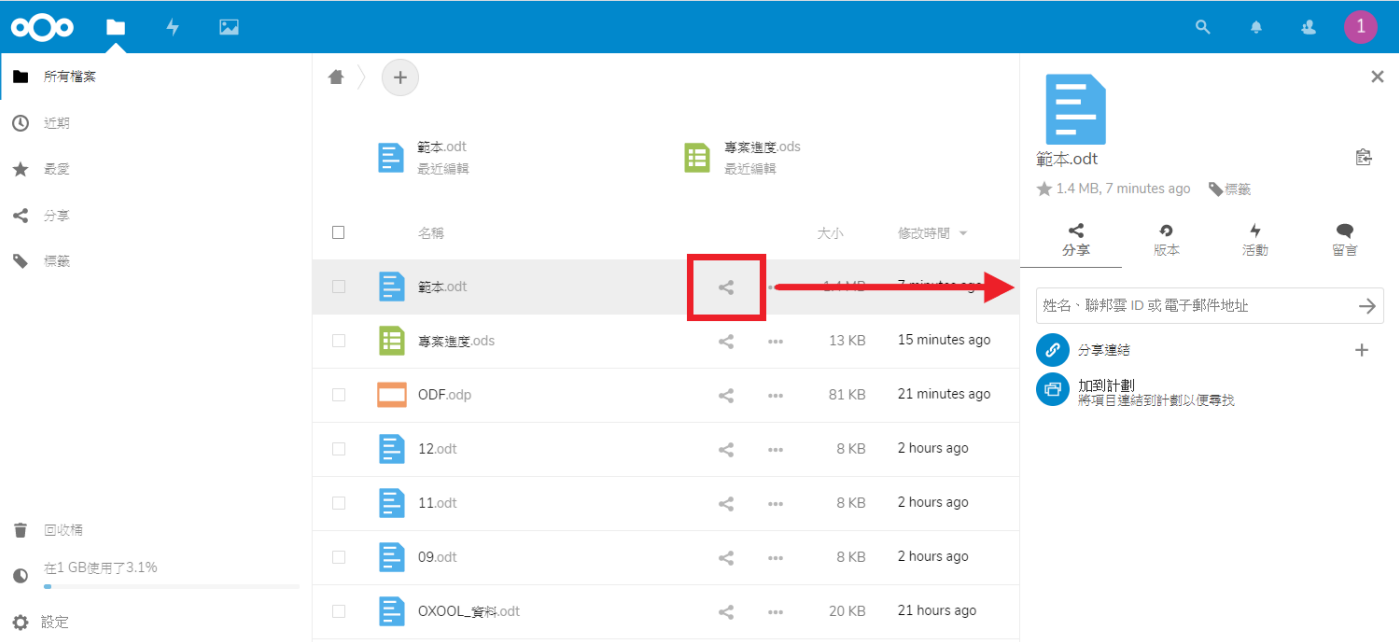





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 給收件人的訊息

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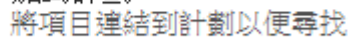
The diagram illustrates the addition of two 8-bit registers. The top row shows two 8-bit registers, each represented by a horizontal rectangle divided into eight vertical segments. An addition sign (+) is placed between them. Below this, a single 16-bit register is shown, represented by a horizontal rectangle divided into sixteen vertical segments. To the right of the registers, the text "URL" is displayed.



標籤



留言

URL

Updated 15 May 2020 09:43:05 by kevin